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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 8484 10/729,875 12/05/2003 Rino Micheloni 02-AG-148 **EXAMINER** 23334 7590 06/14/2005 FLEIT, KAIN, GIBBONS, GUTMAN, BONGINI NGUYEN, VAN THU T & BIANCO P.L. ART UNIT PAPER NUMBER ONE BOCA COMMERCE CENTER 551 NORTHWEST 77TH STREET, SUITE 111 2824 BOCA RATON, FL 33487

DATE MAILED: 06/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
Office Action Summan	10/729,875	MICHELONI ET AL.
Office Action Summary	Examiner	Art Unit
	VanThu Nguyen	2824
The MAILING DATE of this commun Period for Reply	ication appears on the cover sheet with	h the correspondence address
A SHORTENED STATUTORY PERIOD F THE MAILING DATE OF THIS COMMUN - Extensions of time may be available under the provisions after SIX (6) MONTHS from the mailing date of this comm - If the period for reply specified above is less than thirty (3 - If NO period for reply is specified above, the maximum st - Failure to reply within the set or extended period for reply Any reply received by the Office later than three months a earned patent term adjustment. See 37 CFR 1.704(b).	ICATION. s of 37 CFR 1.136(a). In no event, however, may a repnunication. so) days, a reply within the statutory minimum of thirty (satutory period will apply and will expire SIX (6) MONTHY (will, by statute, cause the application to become ABAI	oly be timely filed (30) days will be considered timely. HS from the mailing date of this communication. NDONED (35 U.S.C. § 133).
Status		,
1) Responsive to communication(s) file	ed on	
	2b)⊠ This action is non-final.	
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.		
Disposition of Claims		•
4) ⊠ Claim(s) <u>1-19</u> is/are pending in the a 4a) Of the above claim(s) is/a 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>1,2,9 and 16-19</u> is/are rejected to the company of th	re withdrawn from consideration. cted.	
Application Papers	•	
	er 2003 is/are: a) \square accepted or b) \square oction to the drawing(s) be held in abeyance the correction is required if the drawing(s	e. See 37 CFR 1.85(a). c) is objected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) △ Acknowledgment is made of a claim a) △ All b) □ Some * c) □ None of: 1. △ Certified copies of the priority 2. □ Certified copies of the priority 3. □ Copies of the certified copies	documents have been received. documents have been received in Apple of the priority documents have been received in Bureau (PCT Rule 17.2(a)).	plication No eceived in this National Stage
Attachment(s)	•	
1) Notice of References Cited (PTO-892)	4) Interview Su	mmary (PTO-413)
2) Notice of Draftsperson's Patent Drawing Review (P B) Information Disclosure Statement(s) (PTO-1449 or Paper No(s)/Mail Date		Mail Date Drmal Patent Application (PTO-152) -

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DETAILED ACTION

1. Claims 1-19 are pending.

Claim Rejections - 35 USC § 112

- 2. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 3. Claims 16-19 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 16, lines 9, do applicants really mean "repeatedly applied to the memory cells in the group <u>until</u> the programming state thereof is <u>not</u> ascertained to correspond to a desired programming state"? Same for claim 18, lines 15-18.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 1-2, 9, 16-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shibata et al. (U.S. Patent No. 6,426,892) in view of Norman (U.S. Patent No. 6,438,665).

Regarding claim 1, Shibata discloses, in FIG. 13B, a method of programming an electrically programmable memory, the method comprising:

applying a programming pulse to those memory cells in s group to a desired programming state ("PROGRAM" step ST13); and

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repeating the steps of accessing and applying for the memory cells in the group to the desired programming state ("1st VERIFY READ" step ST14, "2nd VERIFY READ" step ST15, "ARE ALL DATA "1"?" step ST16), wherein

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after the programming state of a prescribed number of memory cells in the group has been ascertained to correspond to a desired programming state (after "1st VERIFY READ" step ST14) accessing again the memory cells in the group and re-ascertaining the programming state of the memory cells whose programming state was previously ascertained to correspond to a desired programming state ("2nd VERIFY READ" step ST15); and

applying at least one additional programming pulse to those memory cells in the group whose programming state is not re-ascertained to correspond to the desired programming state (go back to "PROGRAM" step ST13 if result is "NO" from "ARE ALL DATA "1"?" step ST16).

However, Shibata does not disclose the initial step of accessing a group of memory cells of an electrically programmable memory to ascertain a programming state thereof.

Norman discloses, in Abstract, stored data from flash memory cells are pre-read and compared with new data in order to prevent writing of new data to the flash memory cells.

Since Shibata and Norman are both from the same field of endeavor, the purpose disclosed by Norman would have been recognized in the pertinent art of Shibata.

It would have been obvious at the time the invention was made to a person having ordinary skill in the art compare stored data and new data prior to writing new data into the

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memory cells for the purpose of reducing stress, increasing the operating life, and achieve better system reliability on the memory cells (see Norman, column 7, lines 26-38).

Regarding claims 2, 9, Shibata also discloses that the electrically programmable memory EEPROM (see column 1, lines 21-30) (for claim 2); repeating verifying steps ST14 and ST15 (for claim 9);

Regarding claims 16-19, they are rejected under U.S.C. 103(a) because they recite the same limitations in claims 1-2, except they are drafted in apparatus format. In additional, any integrated circuits are built on a substrate, therefore, "circuit supporting substrate" feature is inherent in Shibata.

Allowable Subject Matter

6. Claims 3-8, 10-15 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VanThu Nguyen whose telephone number is (571) 272-1881. The examiner can normally be reached on Monday-Friday, 9:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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June 10, 2005

VanThu Nguyen

Primary Examiner Art Unit 2824